II. SPECIFICATION AMENDMENTS

Please amend the Abstract of the Disclosure on page 12 as follows:

ABSTRACT OF THE DISCLOSURE

~~POWER EFFICIENT SYMBOL PROCESSING~~

~~The present invention is related to a~~ A symbol rate processing system for high-speed spread spectrum communications is arranged for operation at a specific data rate, ~~comprising~~and comprises programmable hardware blocks running at specific clock frequencies, ~~characterised in that said~~ The system also comprises programmable registers comprising means for interleaving, means for error correction and means for rate matching, wherein said clock frequencies are significantly less than the frequencies needed in a DSP-centric approach.

~~(Figure 1)~~

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